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PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E**

**PATENT DATE**

SCANNED

Q.A. *[Signature]*

1002

|                              |                 |              |                 |                  |                            |
|------------------------------|-----------------|--------------|-----------------|------------------|----------------------------|
| APPLICATION NO.<br>09/961229 | CONT/PRIOR<br>D | CLASS<br>257 | SUBCLASS<br>325 | ART UNIT<br>2811 | EXAMINER<br>J. J. J. J. J. |
|------------------------------|-----------------|--------------|-----------------|------------------|----------------------------|

## APPLICANTS

Vladimir Rumennik  
Donald Disney

the had parent

**TITLE**

High-voltage transistor with multi-layer conduction region

PTO-2040  
12/99

ISSUING CLASSIFICATION

[illegible]

| DRAWINGS                    |             | DATE       |               |
|-----------------------------|-------------|------------|---------------|
| Sketch Drawg.               | App. Drawg. | Print Fig. | Final Drawing |
|                             |             |            |               |
| (Working Drawing)           |             | (Fig. 1)   |               |
|                             |             |            |               |
| (Preliminary Drawing)       |             | (Fig. 2)   |               |
|                             |             |            |               |
| (Final Drawing)             |             | (Fig. 3)   |               |
|                             |             |            |               |
| (Legal Instruments Drawing) |             | (Fig. 4)   |               |
|                             |             |            |               |

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